# UPCOMING AUTOMOTIVE STANDARDS FOR FAULT-TOLERANT COMMUNICATION: FLEXRAY AND OSEKTIME FTCOM.\*

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A safety-critical system needs fault-tolerant communication between its components. This is especially important for automotive domain, as it consists of distributed real-time systems that are based on the results of the communication. To realize distributed systems with predictable time behavior the time-triggered paradigm is used. According to this paradigm, a time-triggered communication protocol, FlexRay, and an operating system OSEKtime with corresponding communication layer FTCom for the fault-tolerant communication are introduced. In this paper we present the formal specifications of FlexRay and FTCom that allow us to argue about their properties in a precise, formal manner and also infer the collaboration between their properties.

#### 1. Introduction

The trend in the automotive industry to shift functionality from machanics and eletronics to software has been going on for several years now, but progress seems to have slowed down. Most of the manufacturers have presented drive-by-wire prototypes, but none of them has entered mass production. The experiences with increased software in the infotainment domain have shown severe quality issues. To overcome this, new technology for distributed fault-tolerant systems, is required.

One major problem today is reliable, deterministic communication for distributed automotive systems. In this domains two standards for automotive systems have established: FlexRay, a fault-tolerant communication

main

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main

network, and OSEK FTCom, a fault-tolerant communication layer for the OSEKtime OS operating system.

The specified system towards the time-triggered paradigm – both, the communication protocol and the operating system, are time-triggered. In a time-triggered system all actions are executed at predefined points in time. This provides a time behavior that is deterministic: task execution times and their order, as well as message transmission times are deterministic. This property is important for distributed real-time systems, because for such kind of systems it is possible to prove their time properties with reasonable effort.

The FlexRay and the FTCom form, together with OSEKtime OS, the verification framework<sup>3</sup> that provides the methodology for the verification of application properties. We abstract here from the detailed specification of the OS, as well as from the application components to concentrate on the representation of the fault-tolerant communication between application components via FlexRay and FTCom.

To make a formal analysis for FlexRay and FTCom possible, several aspects of the systems have been formalized<sup>11</sup>. In this paper, the major aspects of the formalization are described.

## 1.1. FOCUS

This paper is based on the formal language FOCUS<sup>4</sup>. It was chosen since it provides means for modeling concurrent, distributed system and allows to specify them in formal manner.

The central concept in FOCUS are *streams*, that represent communication histories of *directed channels*. Streams in FOCUS are functions mapping the indexes in their domains to their messages. For any set of messages M,  $M^{\omega}$  denotes the set of all streams,  $M^{\infty}$  and  $M^*$  denote the sets of all infinite and all finite streams respectively.  $M^{\omega}$  denotes the set of all timed streams,  $M^{\infty}$  and  $M^{\pm}$  denote the sets of all infinite and all finite timed streams respectively. A *timed stream* is represented by a sequence of messages and *time ticks* (represented by  $\sqrt{}$ ), the messages are also listed in their order of transmission. The ticks model a discrete notion of time.

An empty stream is represented in FOCUS by  $\langle \rangle$ .  $\langle x \rangle$  denotes oneelement stream and #x denotes the length of the stream x. The FOCUS predicate disjunct $(s_1, \ldots, s_n)$  is true, if all streams  $s_1, \ldots, s_n$  are disjunct, i.e. in every time unit only one of these streams has any messages to transfer. To simplify the specification of the real-time systems we introduce the

 $\mathbf{2}$ 

FOCUS operator ti(s, n) that yields the list of messages that are in the timed stream s between the ticks n - 1 and n (at the nth time unit). We also define the FOCUS operator  $maxmsg_n(s)$ , which holds for a timed stream s, if this stream contains at every time unit at most n messages. dom.x denotes [1..#x] and rng.x denotes  $\{x.j \mid j \in dom.x\}$ .

### 1.2. FlexRay

FlexRay is a time triggered communication protocol, developed by the Flex-Ray Consortium<sup>6</sup>. It's primary application domain are distributed real-time systems in vehicles. Today, most of these systems use a Controller Area Network (CAN)<sup>17</sup> as means of communication. The advantages of FlexRay over CAN are: higher bandwidth, integrated functionality for clock synchronisation, deterministic real-time message transmission and fault tolerance.

## 1.3. OSEKtime FTCom

OSEKtime<sup>15</sup> OS is an OSEK/VDX<sup>13</sup> open operating system standard of the European automotive industry. The OSEKtime OS is a time-triggered OS that supports static cyclic scheduling based on the computation of the WCETs (worst case execution times) of tasks. WCETs are needed for scheduleability analysis and can be estimated from a compiled C program and the processor the program runs on<sup>1</sup>.

FTCom<sup>14</sup> (Fault-Tolerant Communication) is an fault-tolerant communication layer for OSEKtime that provides a number of primitives for interprocess communication and makes task distribution transparent.

### 2. Formal Specification

## 2.1. Fault-Tolerant Embedded System

The architecture of the overall system is represented as a FOCUS specification SystemArch. The system consists of a number of nodes that are connected by a FlexRay bus. On each node runs the OSEKtime OS and a number of applications, and on each node there is a component FTComCNI that consists of two subcomponents: the FTCom itself and a CNLBuffer (Communication Network Interface). In the CNI buffer all the messages that must be sent via FlexRay are stored, whereas the local communication on the node is done directly via FTCom.

main



 $= FTComCNI(constant \ t_1^S, \dots, t_n^S \in FType^*; \ t_1^I, \dots, t_n^I \in Id2Slots^*): glass-box = Id2Slots^*$ 



#### 2.2. FTCom

The component FTCom consists of three subcomponents:  $FTCom\_Buffer$ , Replica and RDA (Replica Determinate Agreement<sup>14</sup>). The FTCom buffer is used for the local communication – between applications that are deployed on the same node, so that local messages are not sent via FlexRay. The Replica and RDA components are needed for the fault-tolerant communication with other nodes of the system.



The type  $FT_CNI\_Entity$  represents the type of application messages. It consists of a message identifier of type MessageId and an application data type DataType. The data types  $RCV_Type$  and  $Status_Type$  represent the result types of the standard FTCom functions<sup>14</sup> ttReceiveMessage and ttSendMessage that are used by the applications to access the FTCombuffer.

The Replica component assumes the replication task: one application message is packed into several FlexRay frames using the replication-tables  $t^S$  – an application message will be transported during several FlexRay slots of each communication round. A replication-table is specified as list of type FType, which is defined below.

type  $FType = ft(slot \in Slot, msl \in MessageId^*)$ 

The RDA component assume the RDA task: frames are unpacked using the RDA-tables  $t^{I}$ . A RDA-table is specified as list of type *MessageId*. From these replicated messages the current one is build using some RDA algorithm, e.g. average, majority vote, "pick any" (see also Sect. 3). The

 $\operatorname{main}$ 

main

Replica and RDA tasks are called by the OSEK time dispatcher every communication round. In the FOCUS specification this represented by using request messages of type Request on the channel startR.

We define<sup>11</sup> several properties for the correct Replica- and RDA-tables<sup>a</sup>. For the overall system the same properties must also hold for the unions of the corresponding tables of the overall system, namely gS is the union of all  $t^S$  tables and gI of all  $t^I$  tables. Moreover, the tables gS and gI must be "inverse" in sense of the predicate  $InverseSI^{11}$ . In such a way we can formally show (using a theorem prover Isabelle/HOL<sup>12,21</sup>) for the concrete tables that they are correct according to these properties.

InverseSI

6

 $\begin{array}{ll} gS \in FType \ ^{*}; \ gI \in MessageId \ ^{*} \\ \hline \\ \hline \\ \forall i \in \mathsf{rng}.gI: \ \exists s \in \mathsf{rng}.gS: \ i \in \mathsf{rng}.msl(s) \\ \forall ft(s, \ id\_list) \in \mathsf{rng}.gS: \ \forall i \in \mathsf{rng}.id\_list: \ i \in \mathsf{rng}.gI \\ \forall i, j \in \mathsf{dom}.gS: \ i \neq j \Rightarrow sl(gS.i) \neq sl(gS.j) \end{array}$ 

### 2.3. FlexRay

FlexRay contains a set of complex algorithms to provide the communication services. From the view of the software layers above FlexRay only a few of these properties become visible. The most important ones are static cyclic communication schedules and system-wide synchronous clocks. These provide a suitable platform for distributed control algorithms as used e.g. in drive-by-wire applications. The formalization described here is based on the "Protocol Specification 2.0"<sup>8</sup>.

#### 2.3.1. Abstractions

To reduce the complexity of the system several aspects of FlexRay have been abstracted in this formalization: (1) There is no clock synchronization or start-up phase since clocks are assumed to be synchronous. This corresponds very well with the *time-synchronous* notion of Focus <sup>4</sup>. (2) The model does not contain bus guardians. (3) Only the static segment

<sup>&</sup>lt;sup>a</sup>Like "Every slot identifier can occur in the frame table  $t^S$  at most once", "the table is non-empty", etc.

has been included not the dynamic, as we are mainly interested in timetriggered systems. (4) The time-basis for the system is one slot i.e. one slot FlexRay corresponds to one tick in in the formalization. (5) The system contains only one FlexRay channel. Adding a second channel would mean simply doubling the FlexRay component with a different configuration and adding extra channels for the access to the *CNL-Buffer* component.

## $2.3.2. \ FlexRay \ Architecture$

The component FlexRay Architecture is a refinement of the component FlexRay (see below) and consists of several FlexRay- Controller and a network Cable. The unconnected channels of each controller are to be connected to those of the FTCom-CNI components. Since FOCUS does not contain a concept for broadcast communication this is simulated in the component Cable: It forwards a received frame to all connected nodes.

The type *Slot* describes here one time slot during a FlexRay communication cycle and is equal to the type of natural numbers  $\mathbb{N}$ . A *Frame* that represents a FlexRay frame consists of a slot identifier *slot* and the payload *Payload*. The definition of the type *Payload* depends on the configuration of the FTCom (see Sect. 2.2 and Sect. 3). The FlexRay bus configuration *Config* contains the bus scheduling table *schedule* of the node and the length of the communication cycle *cyclelength*.



 $_{main}$ 

8

The component *FlexRay* contains the assumptions and guarantees for the FlexRay network. In *IdenticCycleLength* it assumes that the length of the communication cycle is identical for all nodes, in *DisjointSchedules* that for each slot of a cycle there is at most one sending node. These are the basic requirements of a static cyclic time division multiplexing network.

-FlexF	Ray (constant $c_1,, c_n \in Config$ )	timed <u>—</u>
in	$return_1,, return_n : Frame$	
out	$store_1,, store_n : Frame; get_1,, get_n : Slot$	
asm	$\forall i \in [1n]$ : maxmsg <sub>1</sub> (return <sub>i</sub> )	
	$DisjointSchedules(c_1,, c_n)$	
	$IdenticCycleLength(c_1,,c_n)$	
gar	$\mathit{Frame Transmission}(\mathit{return}_1,,\mathit{return}_n,\mathit{store}_1,,\mathit{store}_n,\mathit{get}_1$	$,,get_{n},$
	$c_1,c_n)$	
	$orall i \in [1n]$ : $maxmsg_1(\mathit{get}_i) \land maxmsg_1(\mathit{store}_i)$	

The guarantee  $maxmsg_1$  defines that over the channels  $get_i$  and  $store_i$  at most one frame is transmitted in each tick. The former one is required by the *CNL\_Buffer*, the latter by *Cable*. The guarantee *FrameTransmission* describes how frames are transmitted over a FlexRay-network.

FrameTransmission store<sub>1</sub>, ..., store<sub>n</sub>, return<sub>1</sub>, ..., return<sub>n</sub>  $\in$  Frame  $\stackrel{\omega}{=}$ get<sub>1</sub>, ..., get<sub>n</sub>  $\in$  Slot  $\stackrel{\omega}{=}$ c<sub>1</sub>, ..., c<sub>n</sub>  $\in$  Config  $\forall t \in \mathbb{N}, k \in [1..n] :$   $s \in schedule(c_k) : s = t \mod cycleLength(c_k) \rightarrow$   $ti(get_k, t) = \langle s \rangle \land$  $\forall j \in [1..n], j \neq k : ti(store_j, t) = ti(return_k, t)$ 

This predicate specifies that if at time t the node k should be sending according to it's schedule, then it requests the frame which should be sent from the *CNLBuffer* over the channels  $get_k$  and  $return_k$ . This frame is then send to the other nodes of the system. These receive the frame and store

main

it in their respective CNLBuffers over the channel  $store_j$ . This guarantee is the theorem that has to be proved for an implementation of the FlexRay protocol.

## 2.3.3. FlexRay Controller

A FlexRay-Controller component consists of a Scheduler and a BusInterface. The Scheduler evaluates the schedule schedule(c). This schedule specifies in which time slot this node should send a frame. Based on this information the Scheduler notifies the BusInterface in case a frame should be sent. The slot number specifies the time slot when the frame should be sent and also the type of frame, since this mapping is static. The BusInterface then fetches the corresponding frame from the CNL-Buffer and sends it on the channel send. If the node is not sending and a frame is received over the channel recv, this frame is forwarded to the CNL-Buffer over the channel store. For the sake of brevity, the specification details<sup>11</sup> of these components are omitted here.



#### 3. Collaboration between FlexRay and FTCom

The formal specifications of FlexRay and FTCom allow us to argue about their properties in a precise, formal manner<sup>19</sup> and also infer the collaboration between their properties. In this section we discuss the the examples of the collaboration properties.

## 3.1. Level of the Fault-Tolerance

According to the FlexRay specification<sup>8</sup> a frame contains a 24 bit CRC (cyclic redundancy check) checksum to ensure the integrity of the frame transmission. The probability of undetected network errors<sup>16</sup> is less than

10

main

 $6 \cdot 10^{-8}$ , this means that at 10,000 messages per second and a bit error rate of  $10^{-6}$ , this is means approximately  $2 \cdot 10^{-6}$  undetected erroneous frames per hour. Using FTCom's replication mechanisms we can reduce this probability even further and increase the fault-tolerance.

If the FlexRay error rate is good enough for a certain system, we can use simpler configurations of the *RDA* component of the FTCom. Because of the high probability of the communication error detection by the FlexRay, we can assume that if such an error occurs, the wrong frame will be not saved in the CNI buffer. Thus, as the RDA algorithm for this case a "pick first appropriate" can be chosen to make the data processing faster.

The configuration of the replication table for the Replica-task, depends on reliability of physical connection.

## 3.2. FlexRay Frames

The type of payload in the FlexRay frame (see Sect. 2.3) depends on the configuration of FTCom. In the general case, we represent the payload part of the FlexRay frame as a list of application messages – the type *Payload* is then defined as a list over the type  $FT_CNI_Entity$ .

If the number of replicated messages in the system is smaller than the number of the FlexRay communication slots in a round, then it is possible to use the model "One\_message\_per\_frame", in which the types *Payload* and  $FT\_CNI\_Entity$  are equal. In the simple case, when the bus connection is reliable enough to send an application message without replication, i.e. once every FlexRay round, the message identifier can be taken as the corresponding slot number<sup>b</sup> *Payload* = *DataType*.

### 3.3. Schedule Dependences

Combining the time-triggered OS and bus one can synchronize not only the communication, but also the computations in the systems. FlexRay provides OSEKtime OS with a globally synchronized clock. For this purpose, the length of the OSEKtime dispatcher round must be a multiple of the length of the FlexRay round (counted in FlexRay slots).

As mentioned in the Section 2.2, the Replica and RDA tasks must be called by the OSEKtime dispatcher every communication round to have current data both in the FTCom and CNI buffers. Generating the OSEKtime dispatcher tables, this property must be taken into account.

<sup>&</sup>lt;sup>b</sup>This implies that the types *MessageId* and *Slot* ( $\mathbb{N}$ ) are equal.

## 4. Conclusion and Future Work

The paper presented the formal specifications of FlexRay and FTCom in FOCUS and also inferred the correlation between their properties. These FOCUS specifications allow us to argue about the properties of FlexRay and FTCom in a precise, formal manner. Using the presented specification of FlexRay we also have verified<sup>19</sup> that this FlexRay specification conforms the FlexRay requirements.

An Overview of the verification of TTA (Time Triggered Archicecture)<sup>10</sup> was presented by J. Rushby<sup>18</sup>. Since a comparison of TTA and FlexRay<sup>9</sup> have shown several differences, the results of the verification of TTA can not be transfered directly to FlexRay and FTCom.

A verification of the clock synchronization algorithm of FlexRay is in progress at LORIA, based on their framework<sup>5</sup>.

Another future work is the extension of the formal specification with a second FlexRay-channel and Bus Guardians<sup>7</sup>, which would improve the fault-tolerance.

The work on the verification of the lower layers<sup>2</sup> of the specified system is in progress in the Verisoft  $project^{20}$ .

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12

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